Electronic Acknowledgement Receipt				
EFS ID:	2821260			
Application Number:	10797241			
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Title of Invention:	METHOD AND CIRCUIT ARRANGEMENT FOR LIMITING AN OVERVOLTAGE			
First Named Inventor/Applicant Name:	Mark Muenzer			
Customer Number:	58174			
Filer:	Larry L. Coats/Kathleen McDermott			
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Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)	
1	Request for Certificate of Correction	5497020Cert.pdf	53244 189d9402451408c50469e15c0af3ta3d0 70d130a	no	1	

Warnings:

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/IDO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filling Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.